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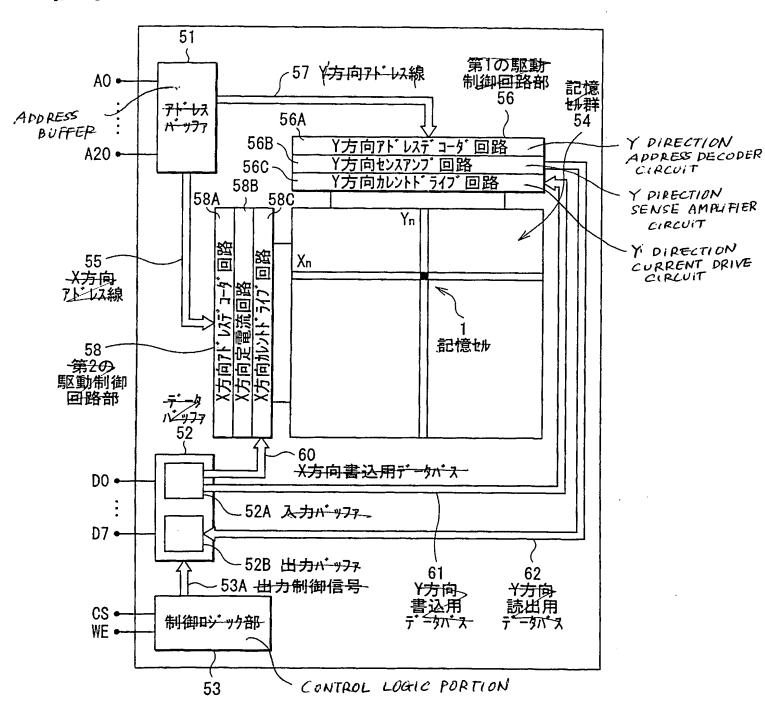
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【書類名】

図面

【図1】

Fig. 1



58A. X DIRECTION ADPRESS DECODER CIRCUIT

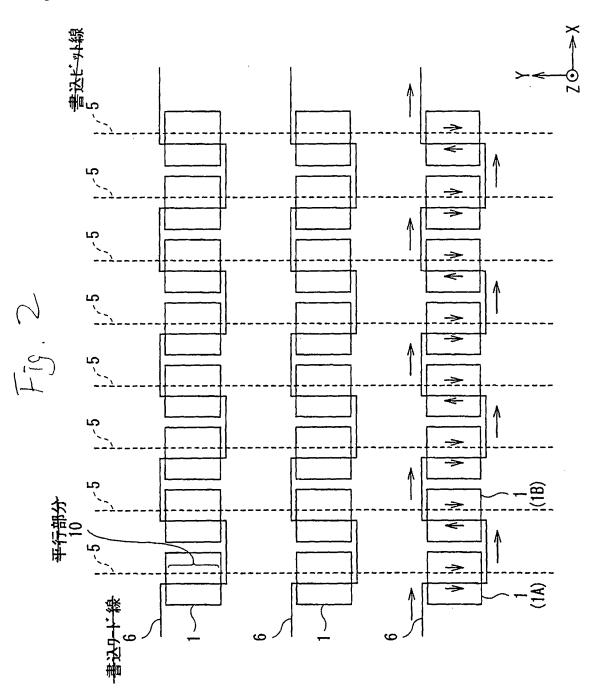
588 X DIRECTION CONSTANT CURPENT CIRCUIT

18C X DIRECTION CURRENT DRIVE CURCUIT

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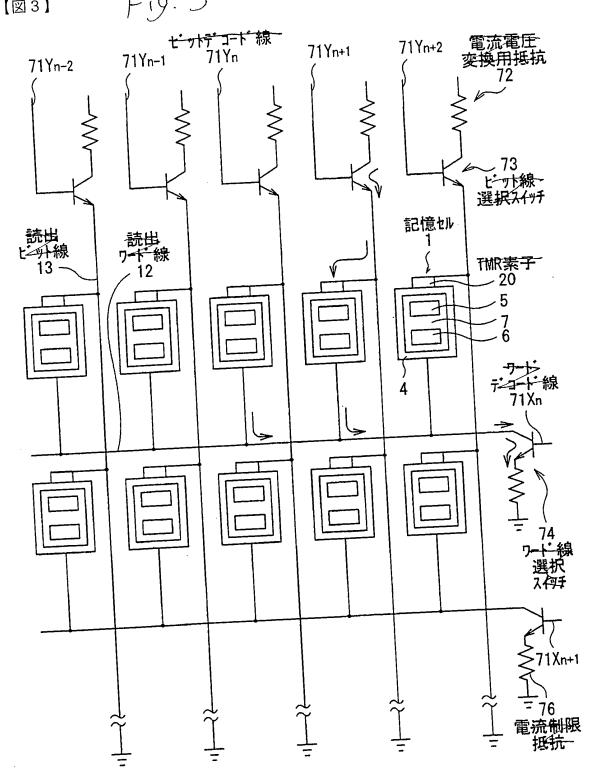
【図2】



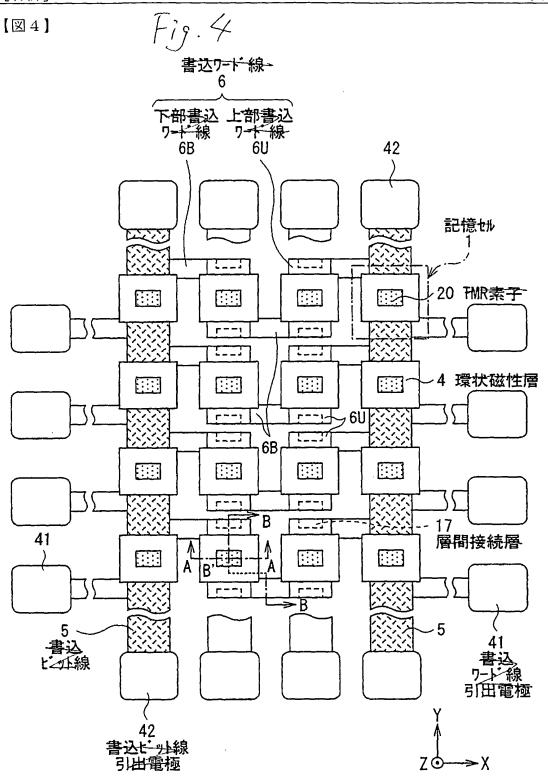
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Fig. 3 【図3】



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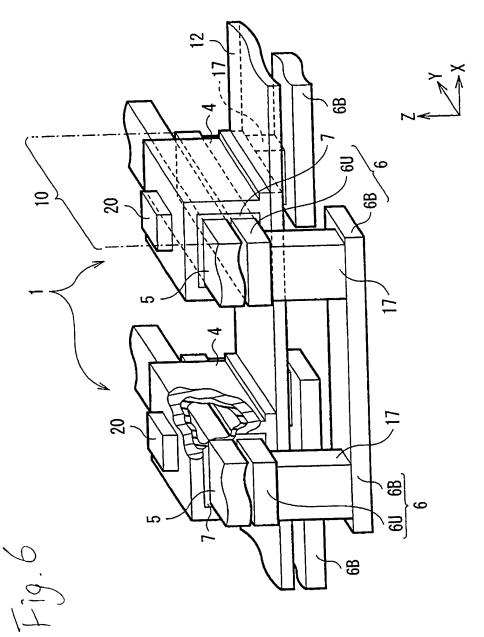
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Fig. 5 【図5】 読出ワード線 引出電極 43 読出 7士線 12 -13 読出 に対線

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【図6】

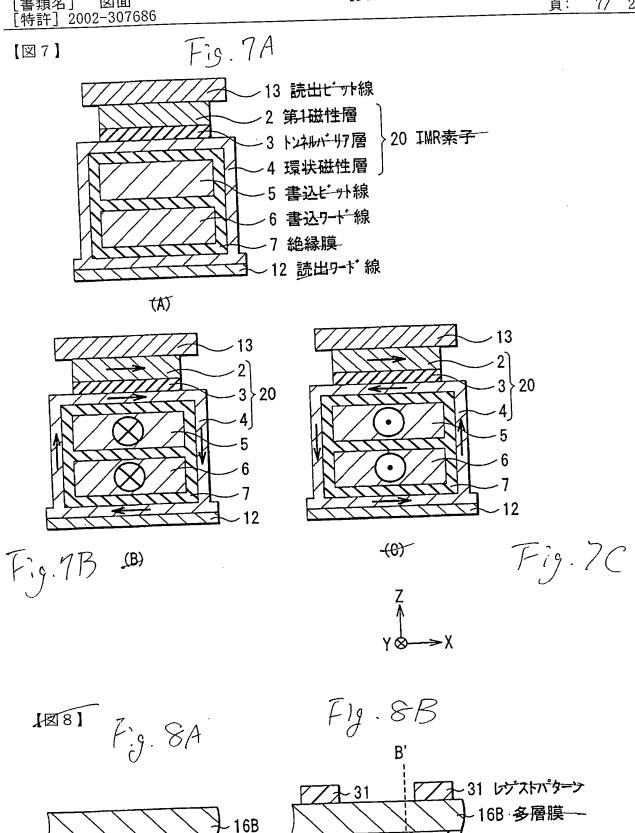


(A)

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11 基板

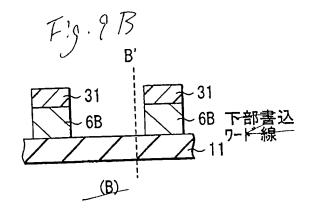
(B)



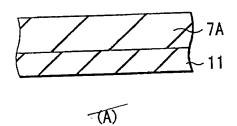
[图9]

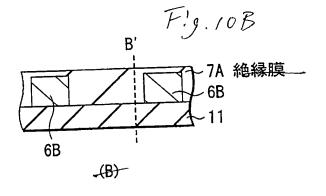
F19.9A





[图10] Fig. 10A

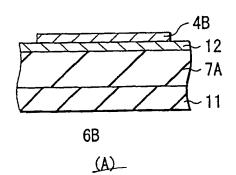


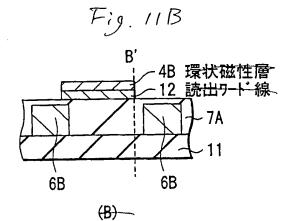


Z ↑ Y ⊗---> X



[21-1] Fig. 11A

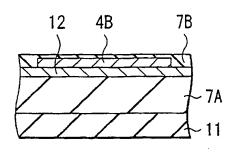




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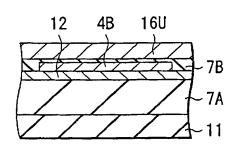
图121

F19. 12A



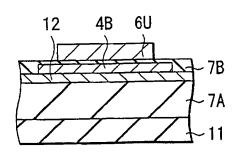
-(A)-

(1813) Fig. 13 A

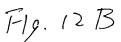


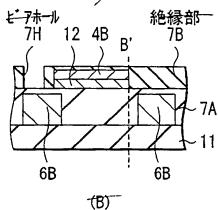
(A)
Z
↑
Y⊗→X

10141 Fig. 14A



(A)





F19.13B

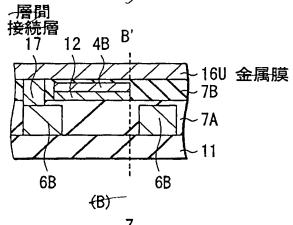
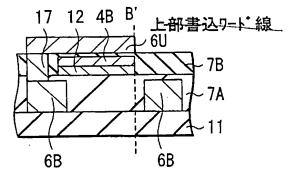




Fig. 14B

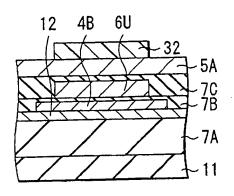


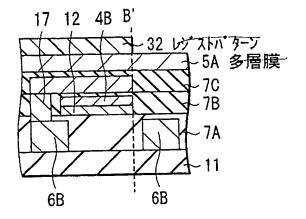
(B)_

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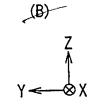
12151 Fig. 15A

Fig. 15B

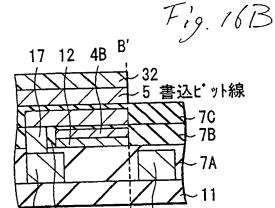




Z ↑ Y⊗—>X



[1816] Fig. 16A



6B

12 4B 6U 32 5 7C 7B 7A

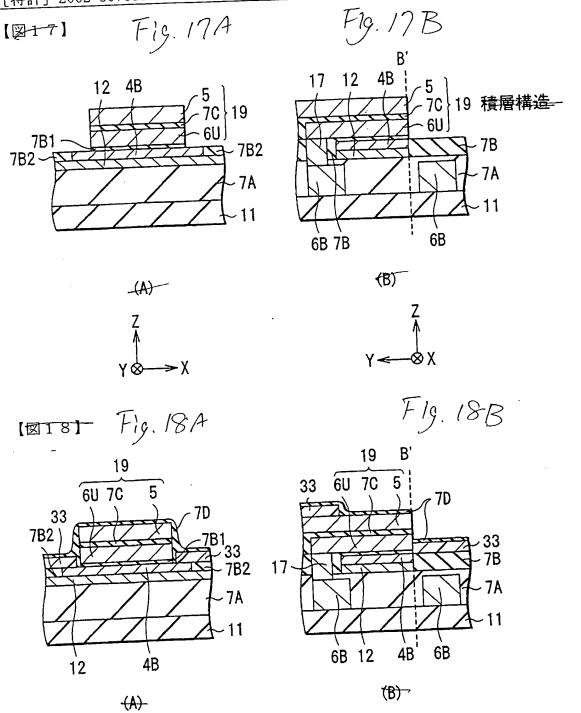
(A)

(B)

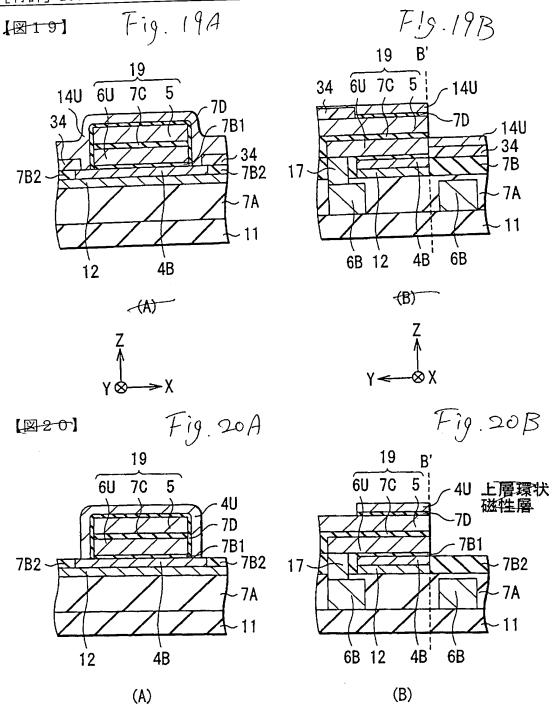
6B 7B

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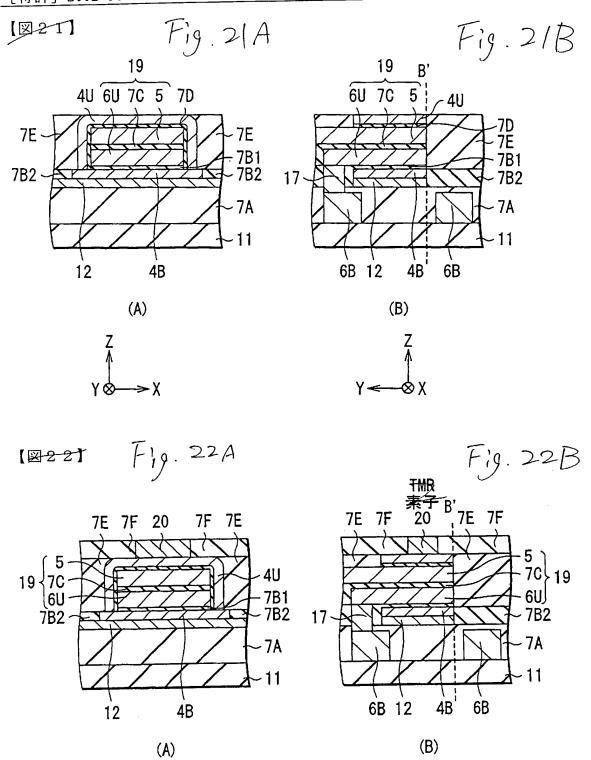
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頁: 12/ 22



頁: 13/ 22

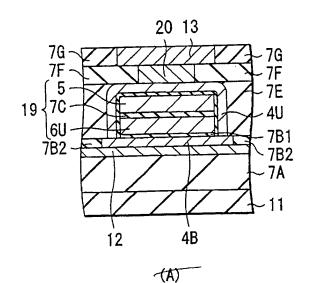


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图23

Fig. 23/A



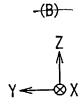
F1g. 23 B
7E 13 7F 20 7F 7E
7G
5
7C 19
6U
77B2

- 7A

6B

4B

Z ↑ Y ⊗---> X

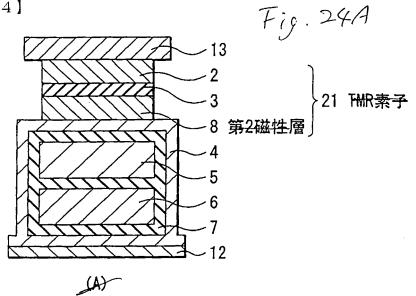


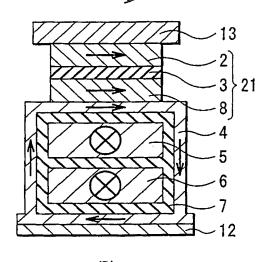
6B 12

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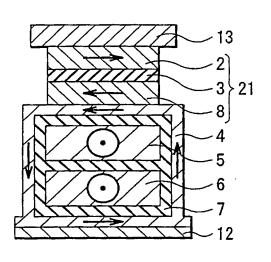
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【図24】





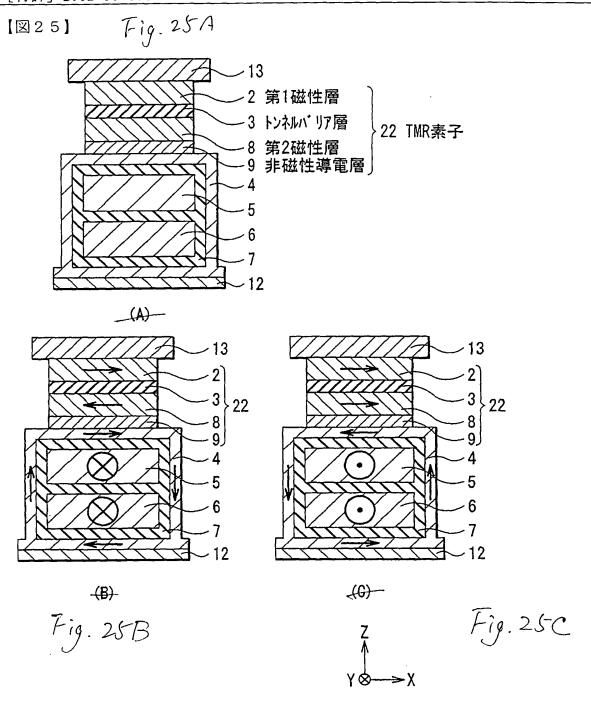
-(B) Fig. 24B



7€ Fig. 2 € C

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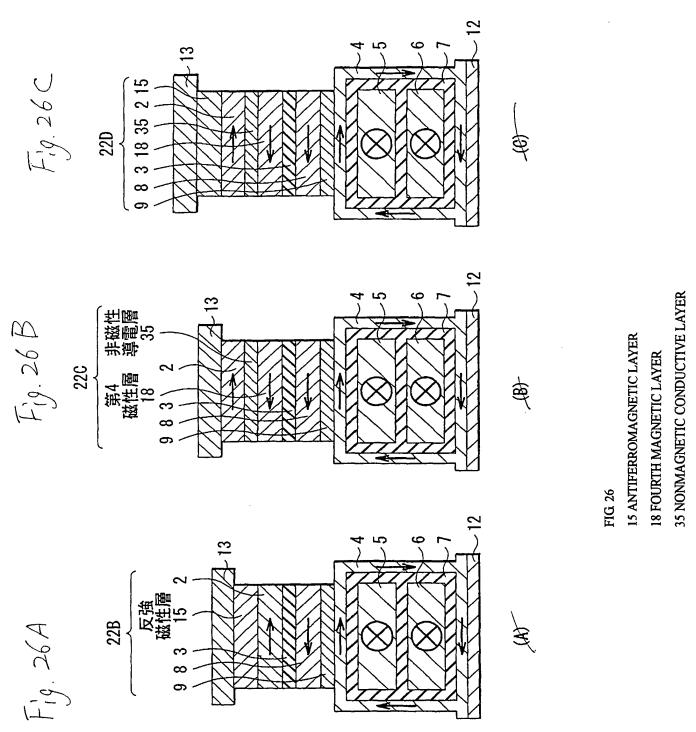
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2 FIST MAGNETIC LAYER, 3 TUNNEL BARRIER LAYER, 8 SECOND MAGNETIC LAYER, 9 NONMAGNETIC CONDUCTIVE LAYER, 22 TMR ELEMENT

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【図26】



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图27]

Fig. 27

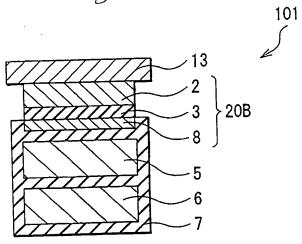
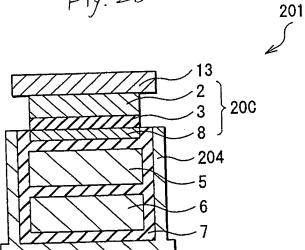


图28

Fig. 28





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【図29】

Fig. 29A

-(A)

Fig. 29B

(B)

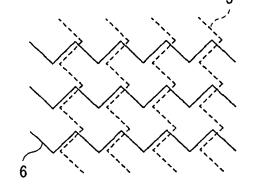
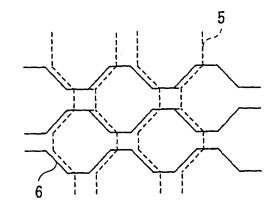
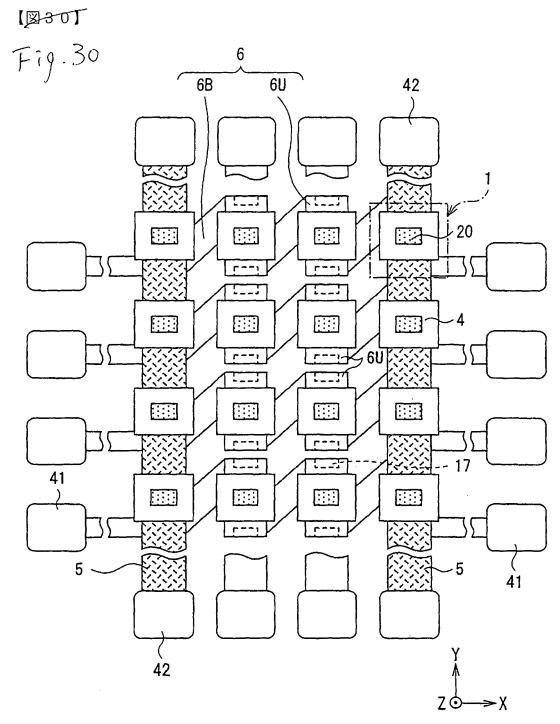


Fig. 29C

(c)



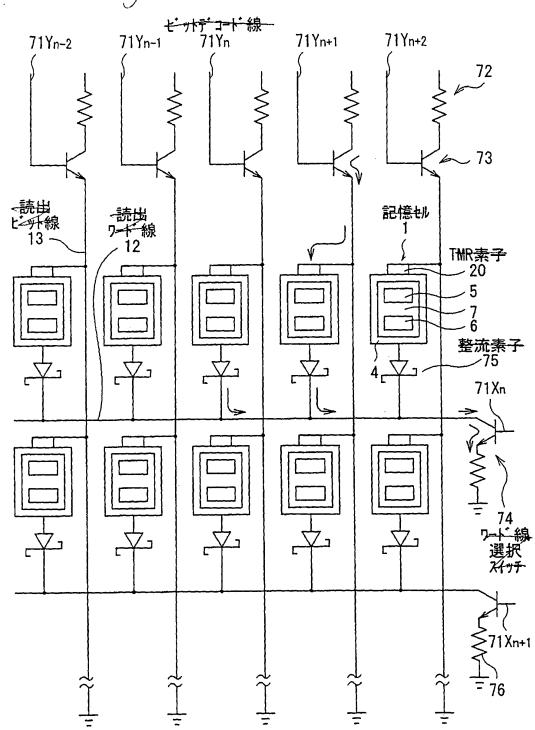
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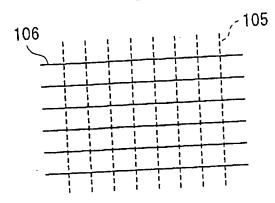
18311 Fig. 31



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12321 Fig. 32





[図33]

Fig. 33

